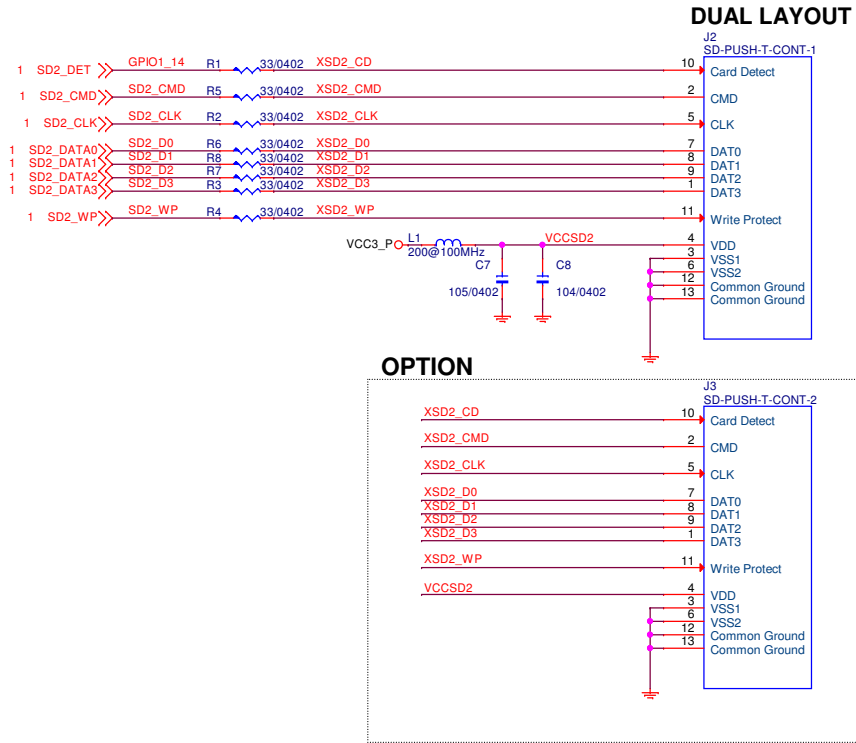


**i.MX8M-DIP168 MODULE**

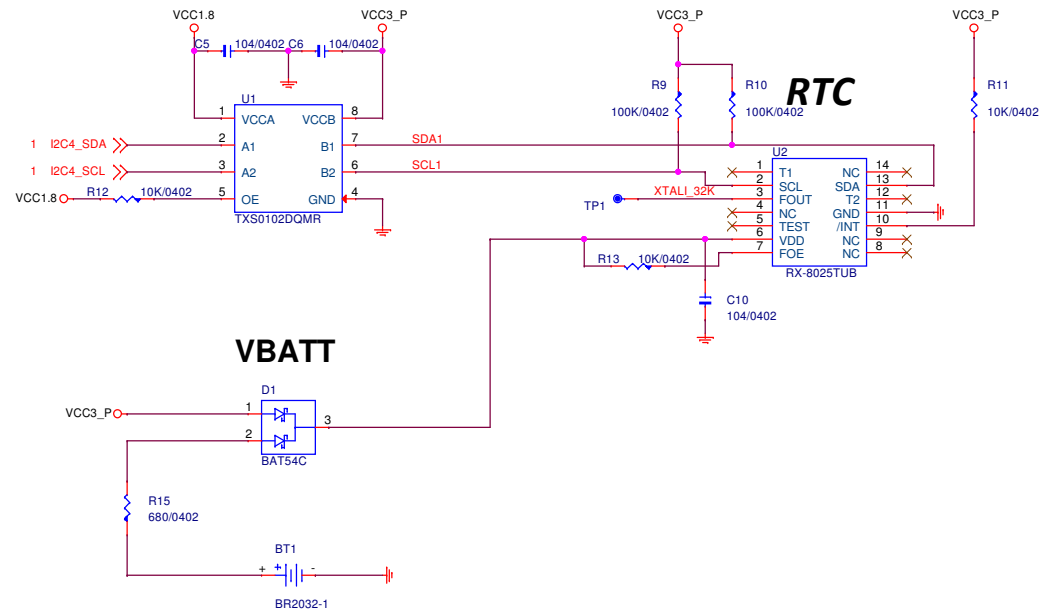
**DM395B  
i.MX8M-DIP168 VESA-PC  
V1.2**

<b>DMP ELECTRONICS INC.</b>		
Title: CPU Board Connector		
Size: DM395B	Document Number: DM395B	Rev: 1.2
Date: Wednesday, February 19, 2020	Sheet: 2	of 19

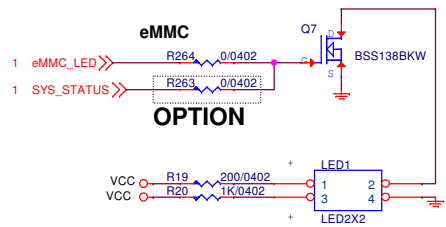
# SD CARD



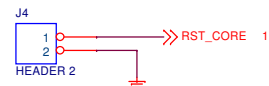
# RTC/VBATT



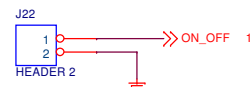
# LED



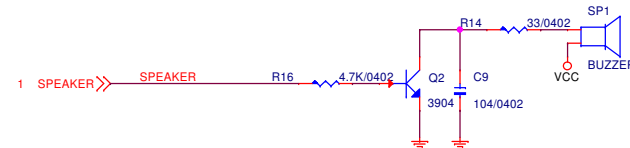
# H/W RST



# ON\_OFF



# SPEAKER



**DMP ELECTRONICS INC.**

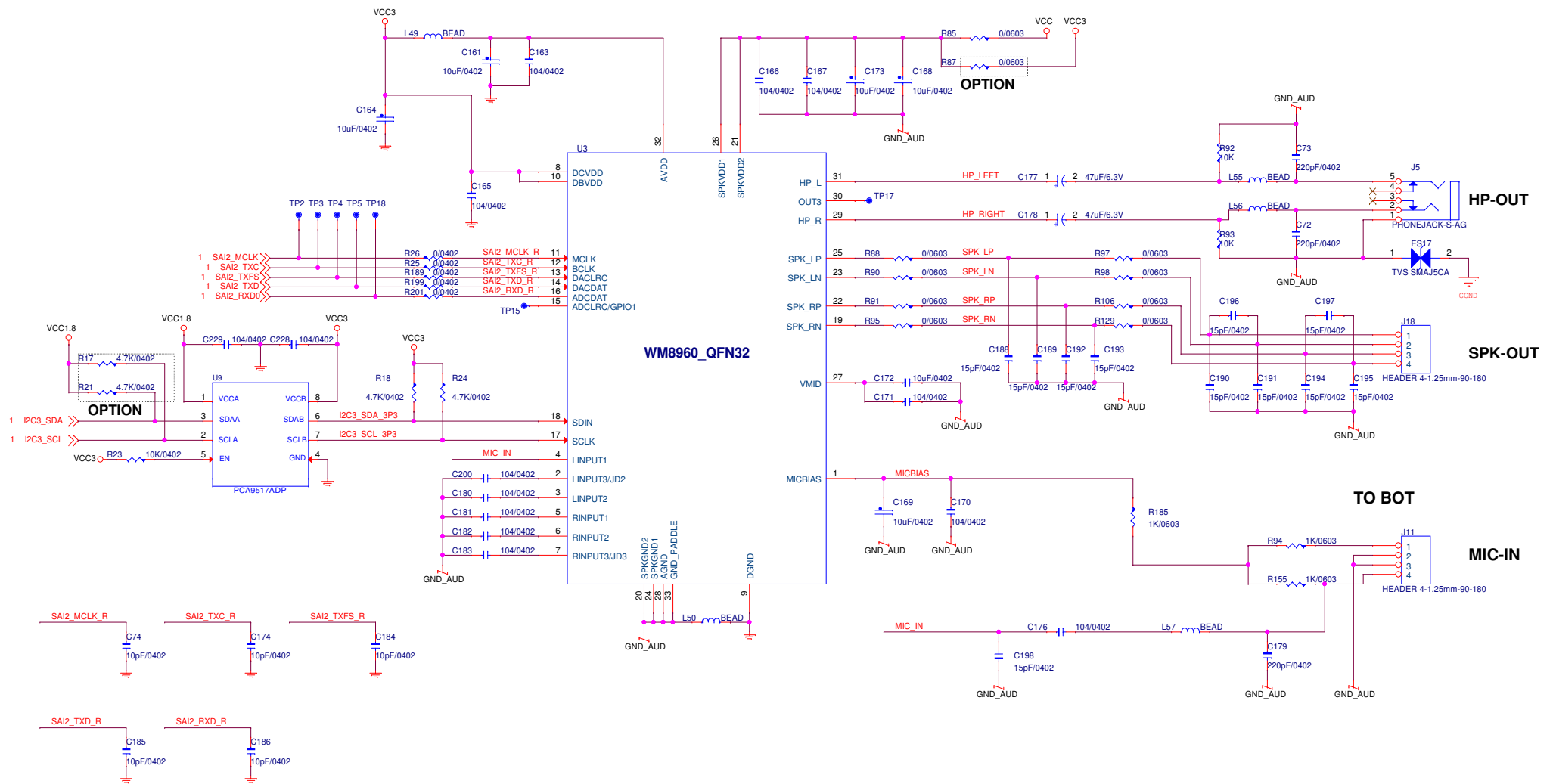
Title: SD Card/SPAKER/RTC VBAT/RST/LED

Size: Document Number DM395B Rev 1.2

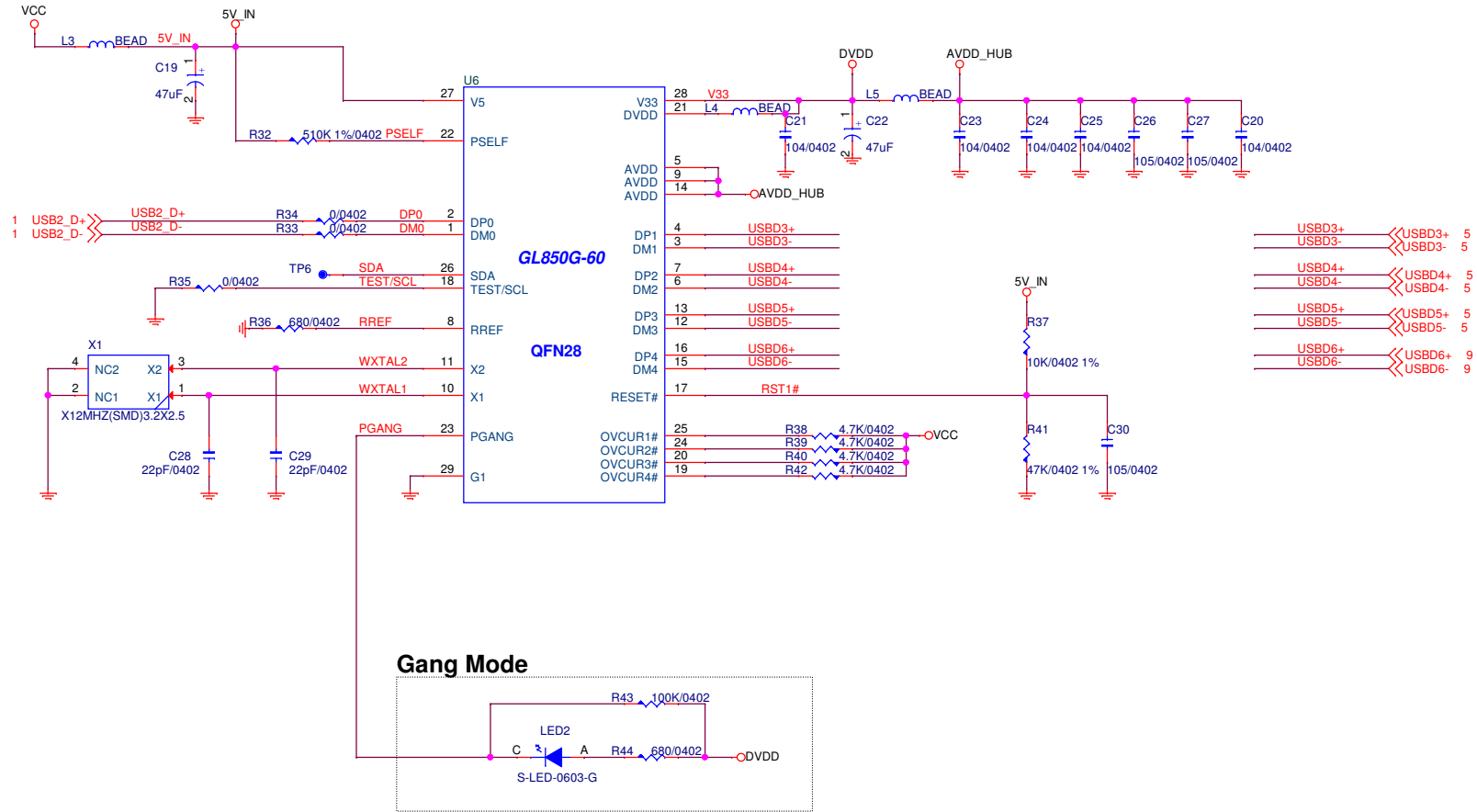
Date: Wednesday, February 19, 2020 Sheet 3 of 19

# AUDIO

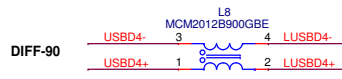
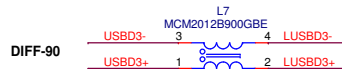
## Audio CODEC



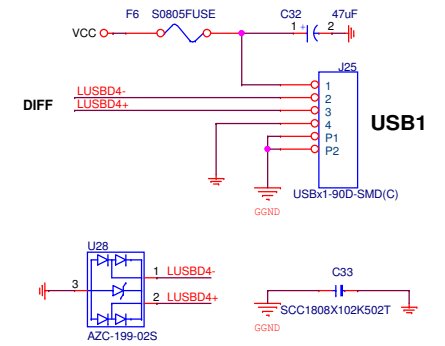
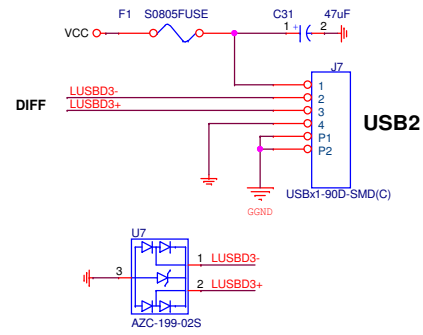
# USB HUB



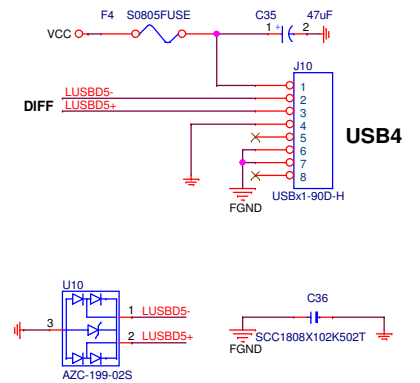
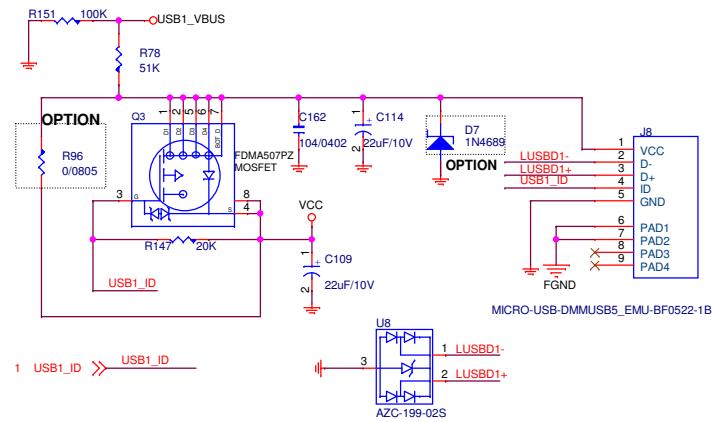
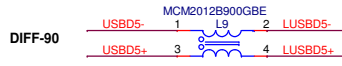
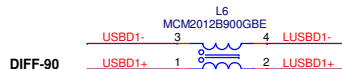
# USBx4



## FRONT USB x2



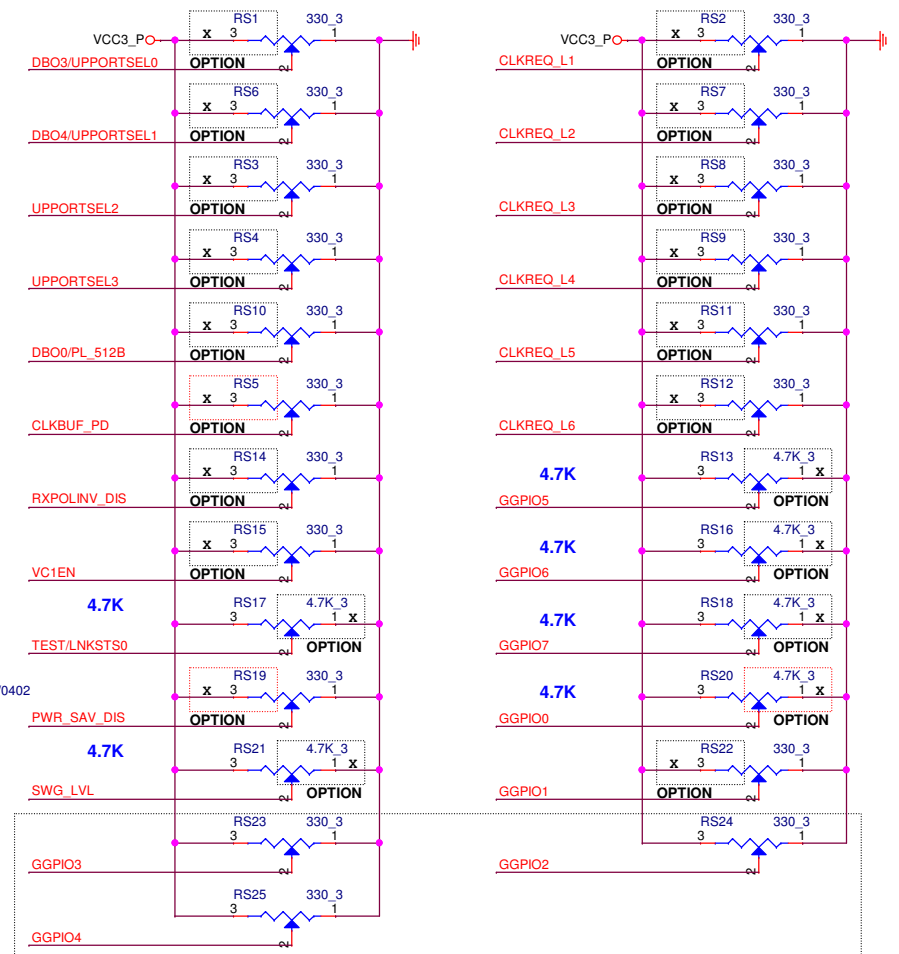
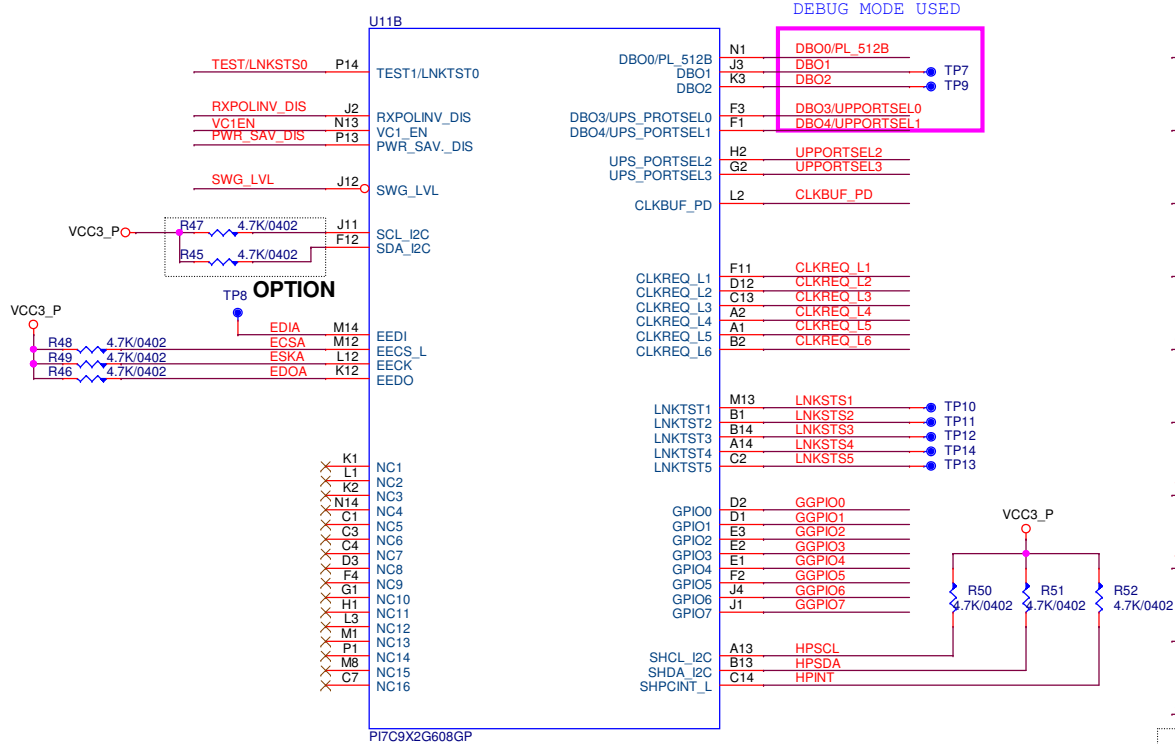
## BACK USB x2



DMP ELECTRONICS INC.

Title		
USBX4		
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# PCIe Bridge-1



PULL VCC3=4.7K  
PULL GND=330

X=NO MOUNT

### PORT-LANE MAPPING

GPIO[1]	GPIO[0]	Functional Mode
0	0	505 mode,5Port-5Lane Configuration
0	1	606 mode,6Port-6Lane Configuration
1	0	508 mode,5Port-8Lane Configuration

Part # PLCSEL = 606GP 01

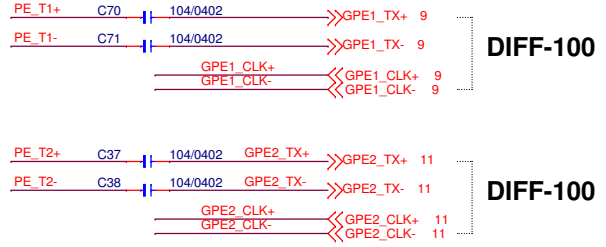
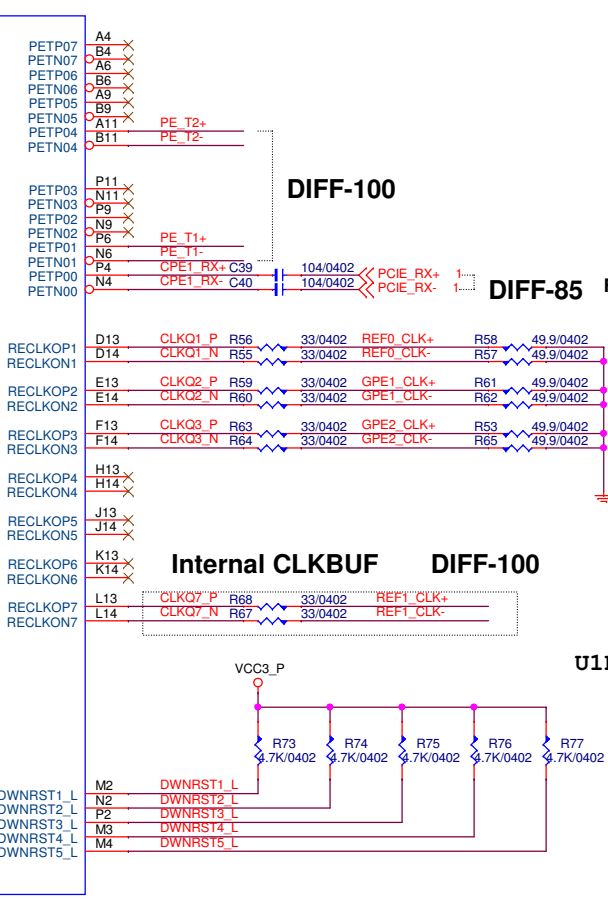
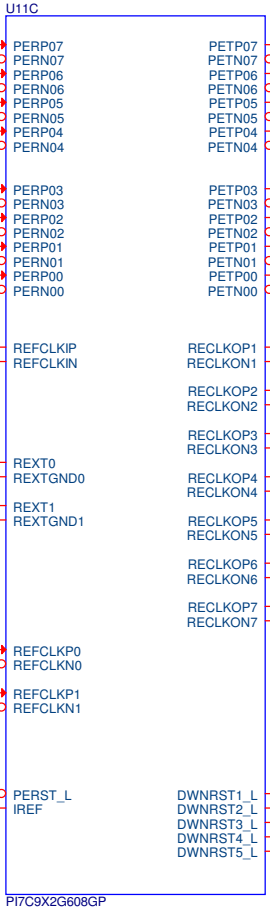
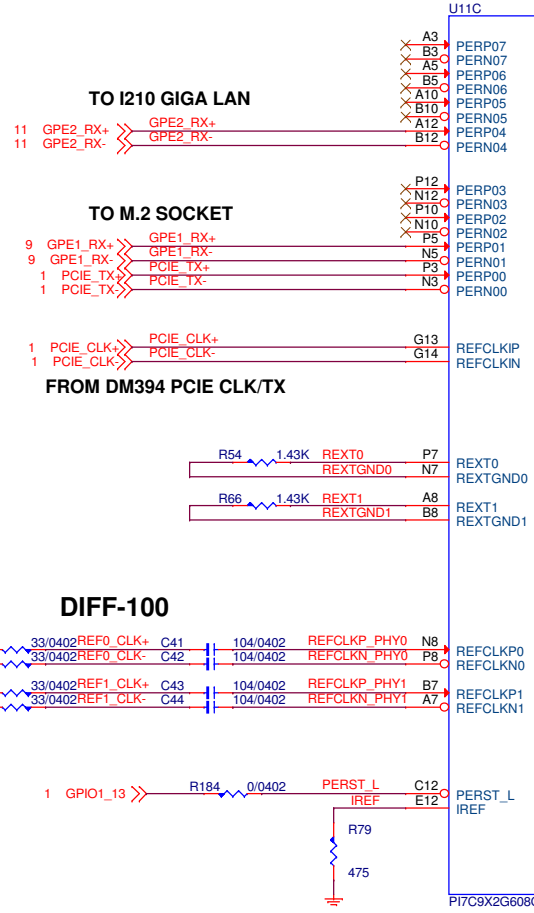
"CLKBUF\_PD" PULL GND  
=Integrated Reference Clock

## DMP ELECTRONICS INC.

Title PCIe Bridge P7C9X2G608GP		
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# PCIe Bridge-2

508	606	505
Port[4]	Port[4]	Port[4]
Port[3]	Port[3]	Port[3]
Port[2]	Port[2]	Port[2]
Port[1]	Port[1]	Port[1]
Port[0]		
Port[0]		
Port[0]	Port[5]	
Port[0]	Port[0]	Port[0]



DIFF-100  
DIFF-85

DIFF-100

DIFF-85 FROM DM394 PCIe RX

DIFF-100

Internal CLKBUF DIFF-100

GPIO[1:0]	Functional Mode					
	606	606	606	606	606	606
UPS_PORTSEL[3:0]	0000	0001	0010	0011	0100	0101
Lane0	P0	P1	P2	P3	P4	P5
Lane1	P5	P5	P5	P5	P5	P0
Lane2	-	-	-	-	-	-
Lane3	-	-	-	-	-	-
Lane4	P1	P0	P1	P1	P1	P1
Lane5	P2	P2	P0	P2	P2	P2
Lane6	P3	P3	P3	P0	P3	P3
Lane7	P4	P4	P4	P4	P0	P4

Note:  
\*  
P0:Upstream port  
P1~P5:downstream port

**DMP ELECTRONICS INC.**

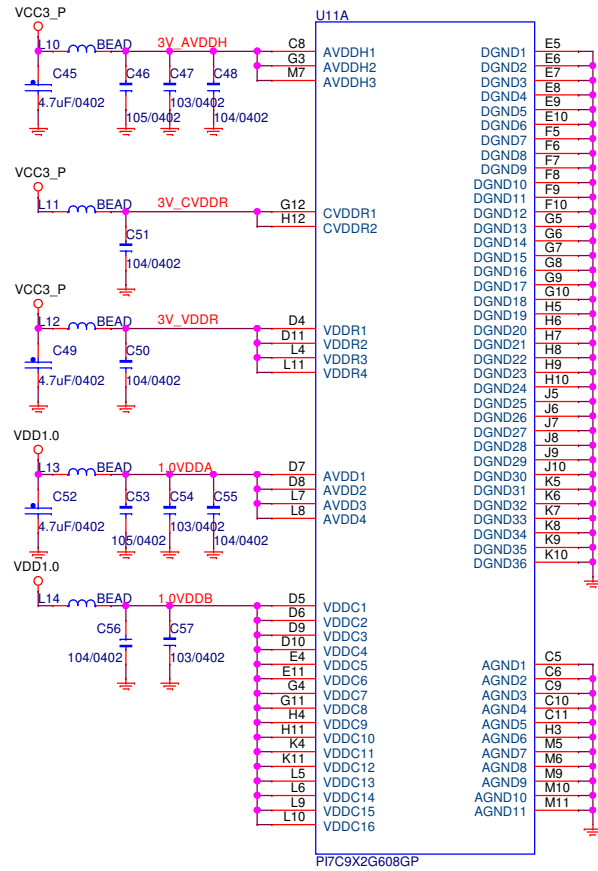
Title: PCIe Bridge Diff Pair/CLK

Size: Document Number DM395B Rev 1.2

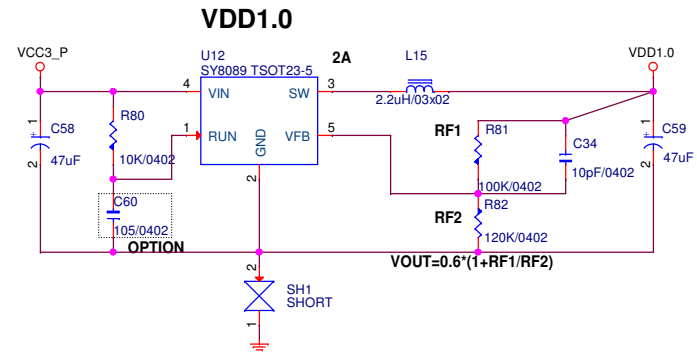
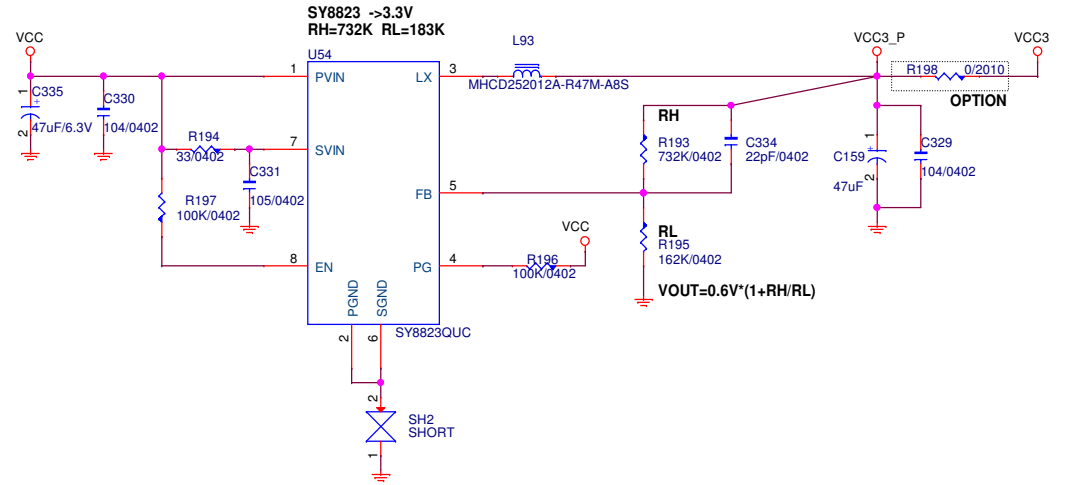
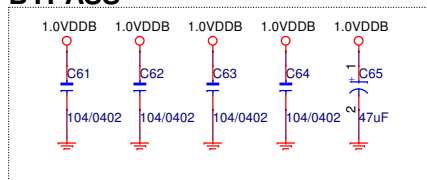
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# PCIe Bridge-3 Power

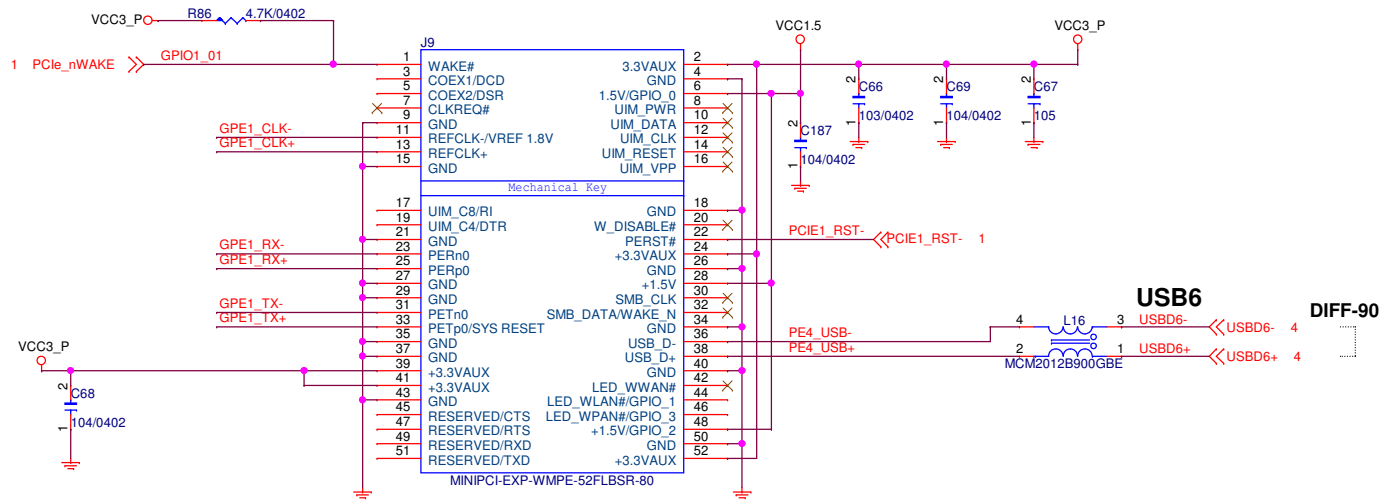


## BYPASS

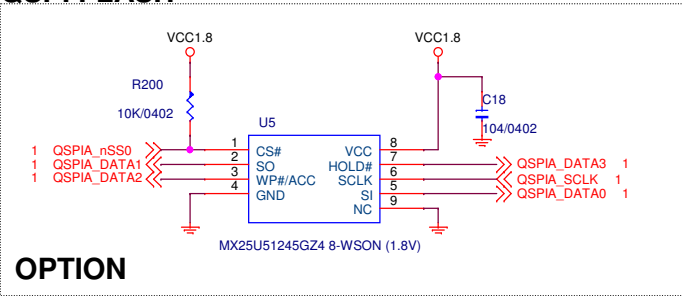


**Power Sequence: +3.3V=>+1.0V**

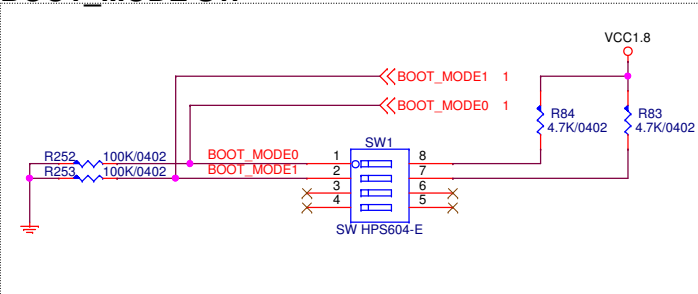
<b>DMP ELECTRONICS INC.</b>			
Title: PCIe Bridge Power			
Size:	Document Number: DM395B	Rev: 1.2	
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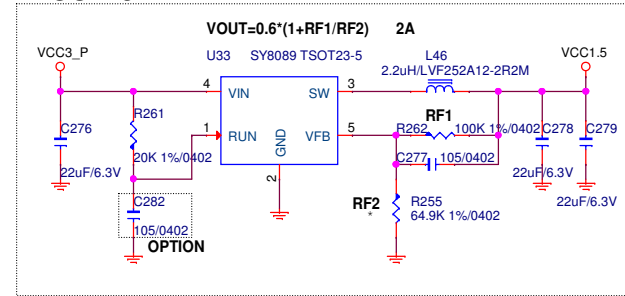
### QSPI FLASH



### BOOT\_MODE SW



### VCC 1.5V

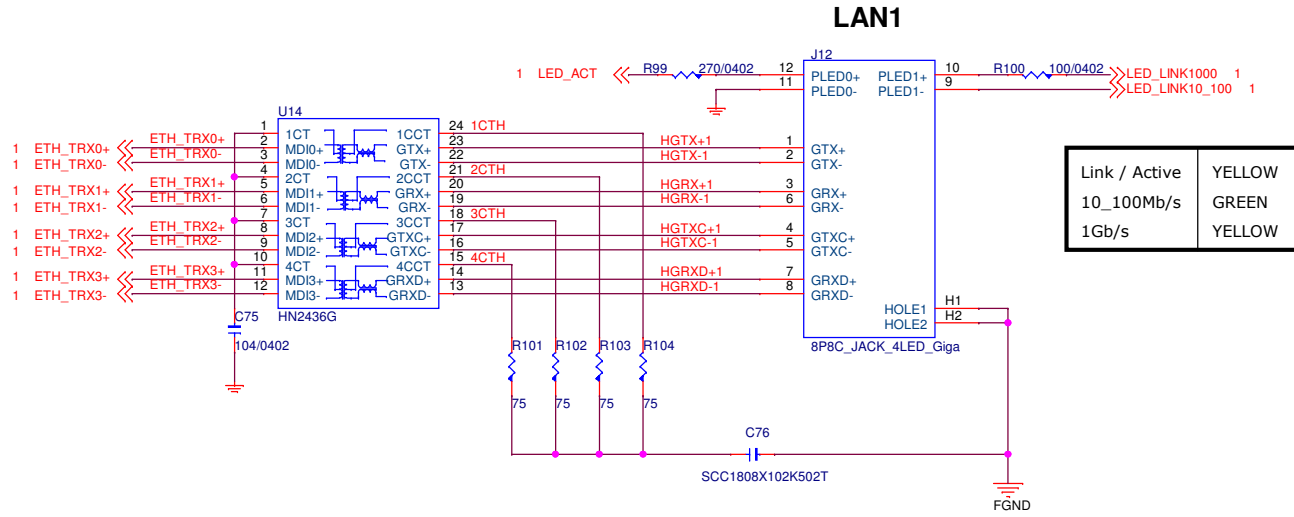


i.MX8M MINI		
MODE0	MODE1	Boot Source
0	0	Boot from fuses
1	0	Serial downloader
0	1	Internal boot
1	1	Reserved

### DMP ELECTRONICS INC.

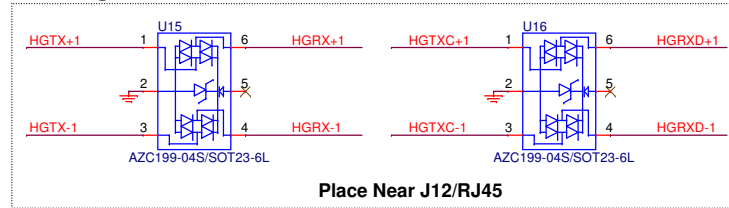
Title MINIPCI-EXP52		
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# RGMII 10/100/1000Mbps LAN-1



Link / Active	YELLOW
10_100Mb/s	GREEN
1Gb/s	YELLOW

## LAN ESD

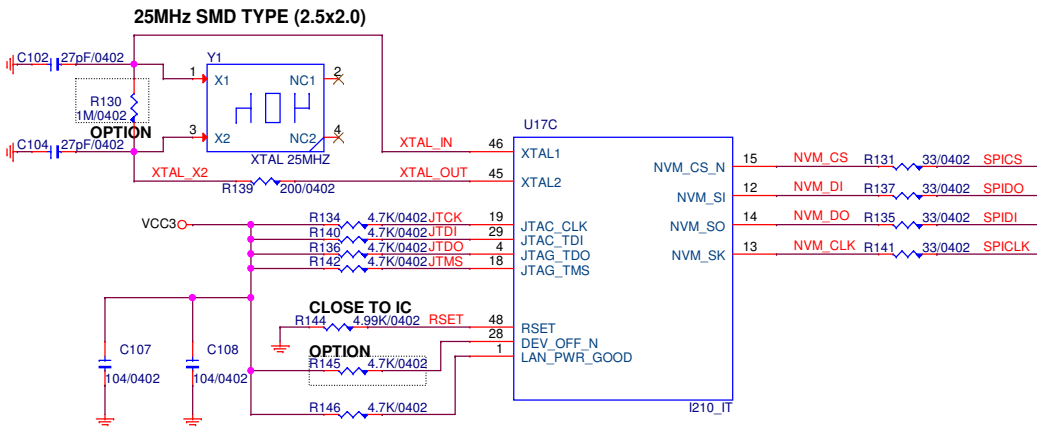
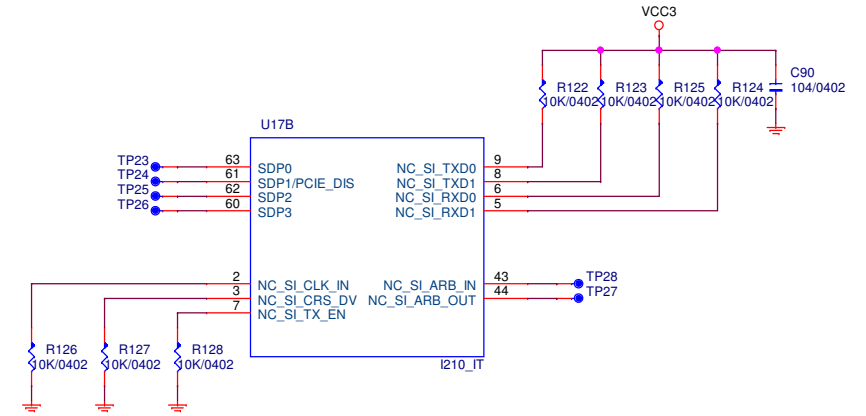
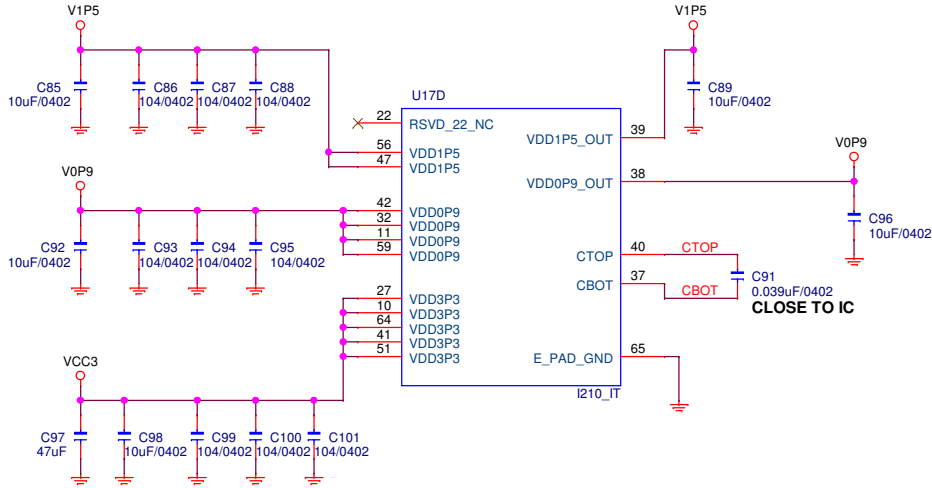


**DMP ELECTRONICS INC.**

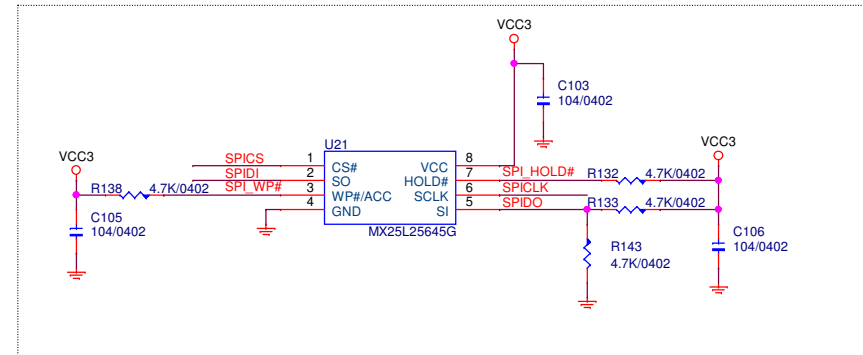
Title			Rev
LAN1			
Size	Document Number	Rev	
	DM395B	1.2	
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# I210 POWER



## OPTION

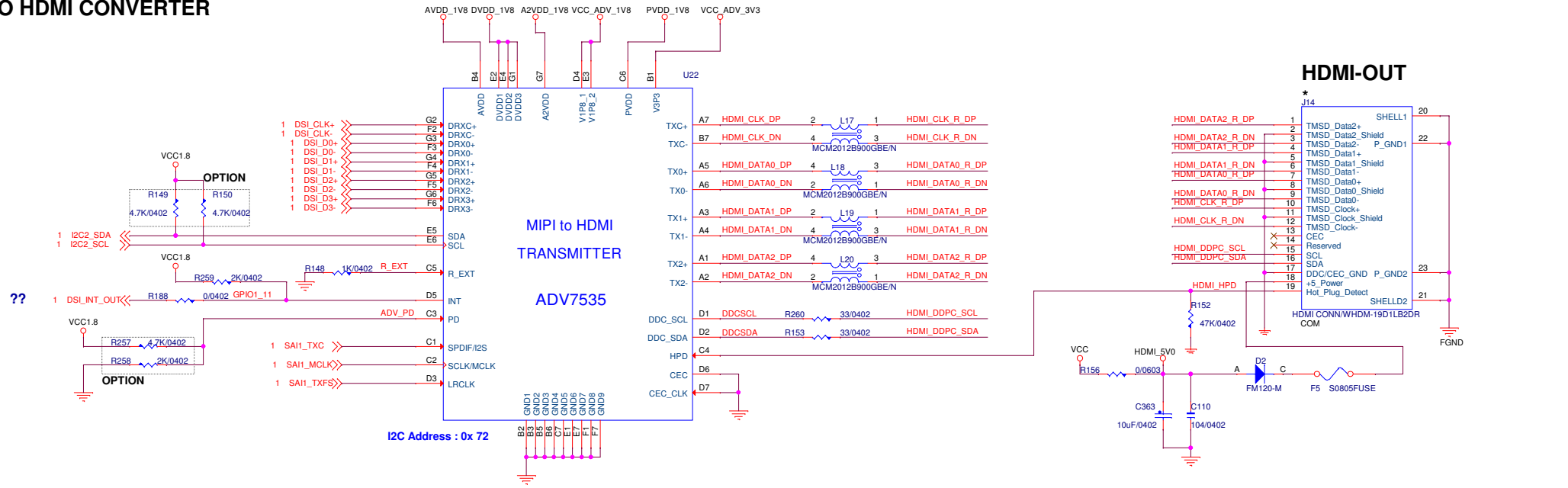


**DMP ELECTRONICS INC.**

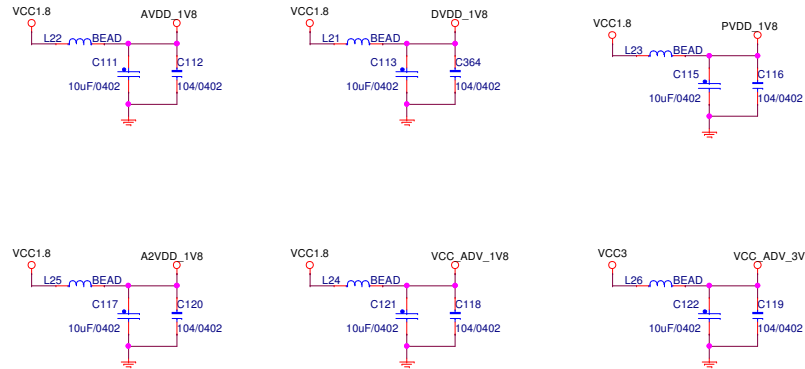


Title		
LAN2 I210 POWER		
Size	Document Number	Rev
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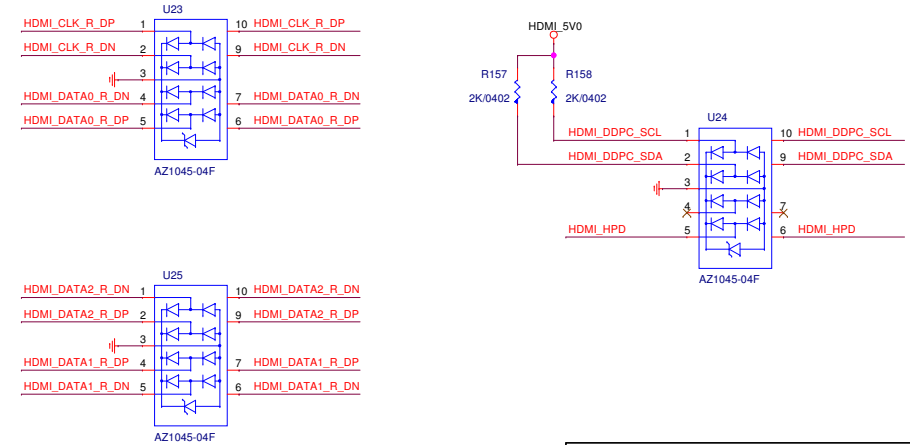
# MIPI TO HDMI CONVERTER



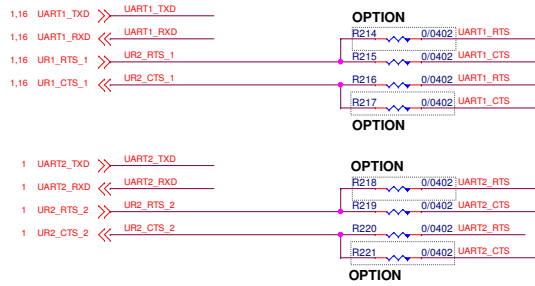
## POWER



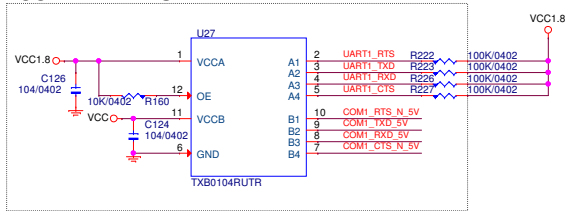
## ESD



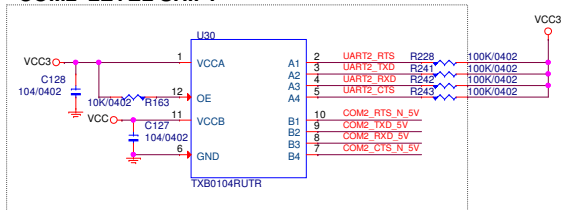
# COM1/COM2



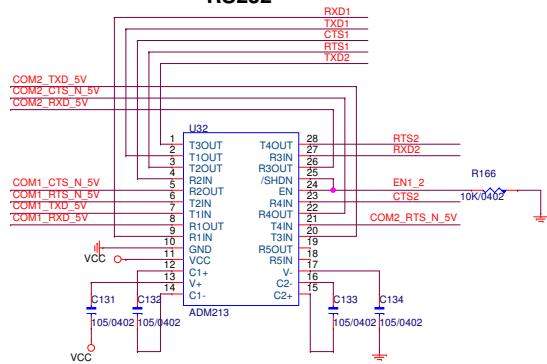
## COM1 LEVEL SHIFT



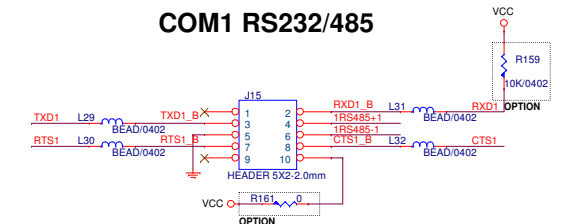
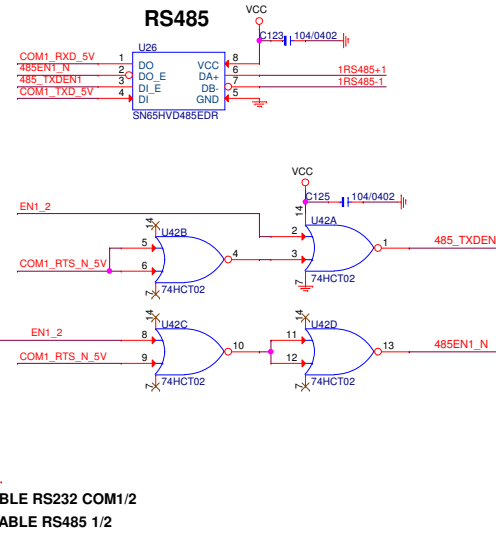
## COM2 LEVEL SHIFT



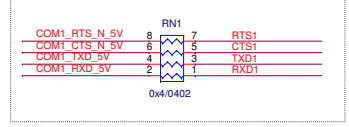
## RS232



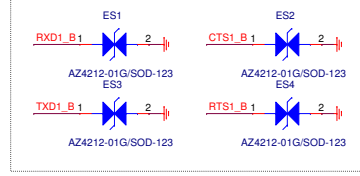
# COM1 RS232/485



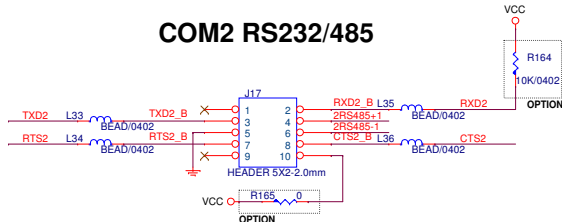
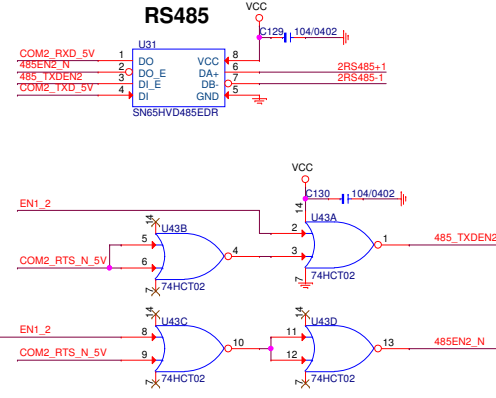
## TTL OPTION



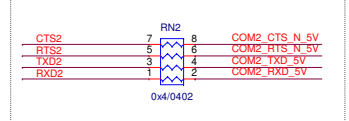
## ESD



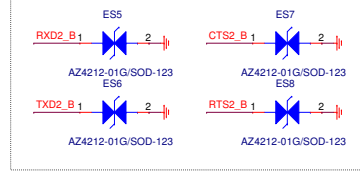
# COM2 RS232/485



## TTL OPTION



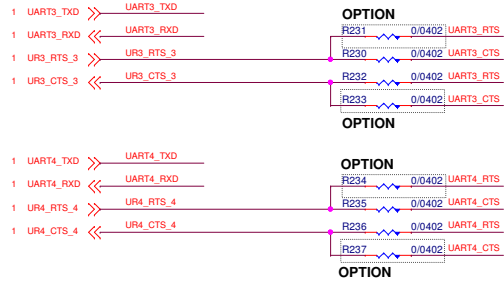
## ESD



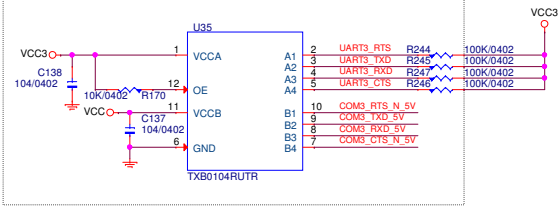
## DMP ELECTRONICS INC.

Title			COM1&2 RS232/485
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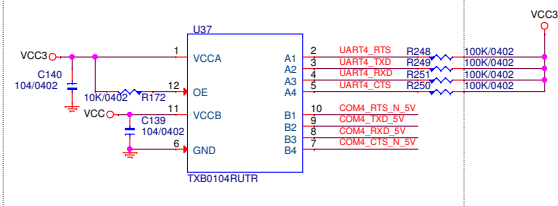
# COM3/4



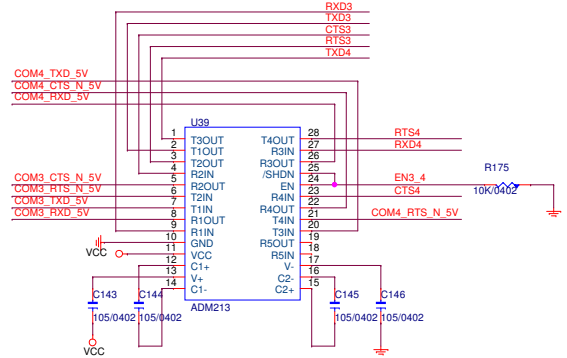
## COM3 LEVEL SHIFT



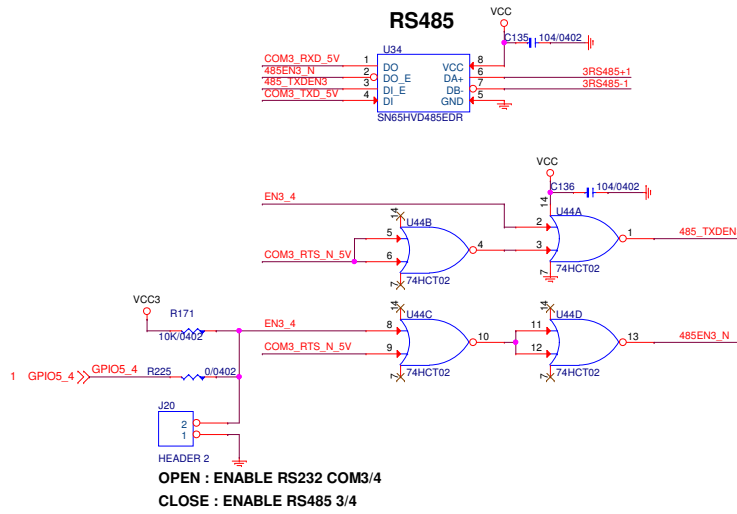
## COM4 LEVEL SHIFT



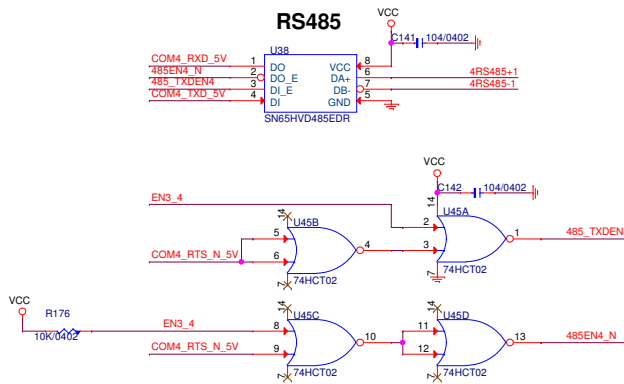
## RS232



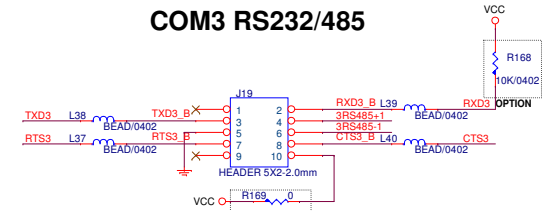
## RS485



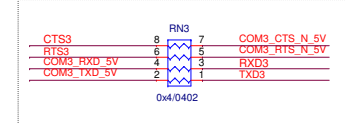
## RS485



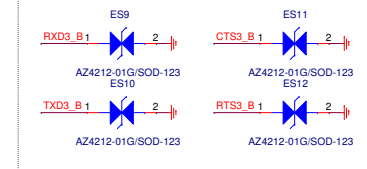
## COM3 RS232/485



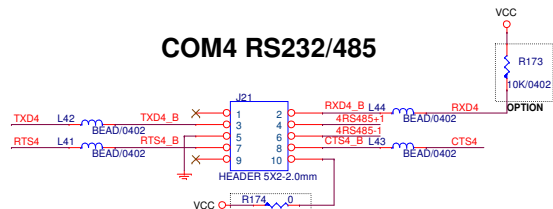
## TTL OPTION



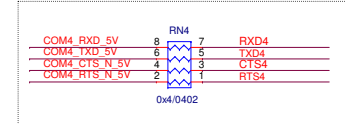
## ESD



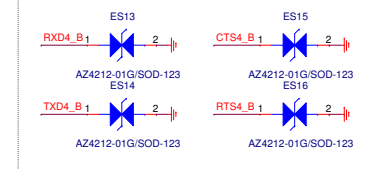
## COM4 RS232/485



## TTL OPTION



## ESD

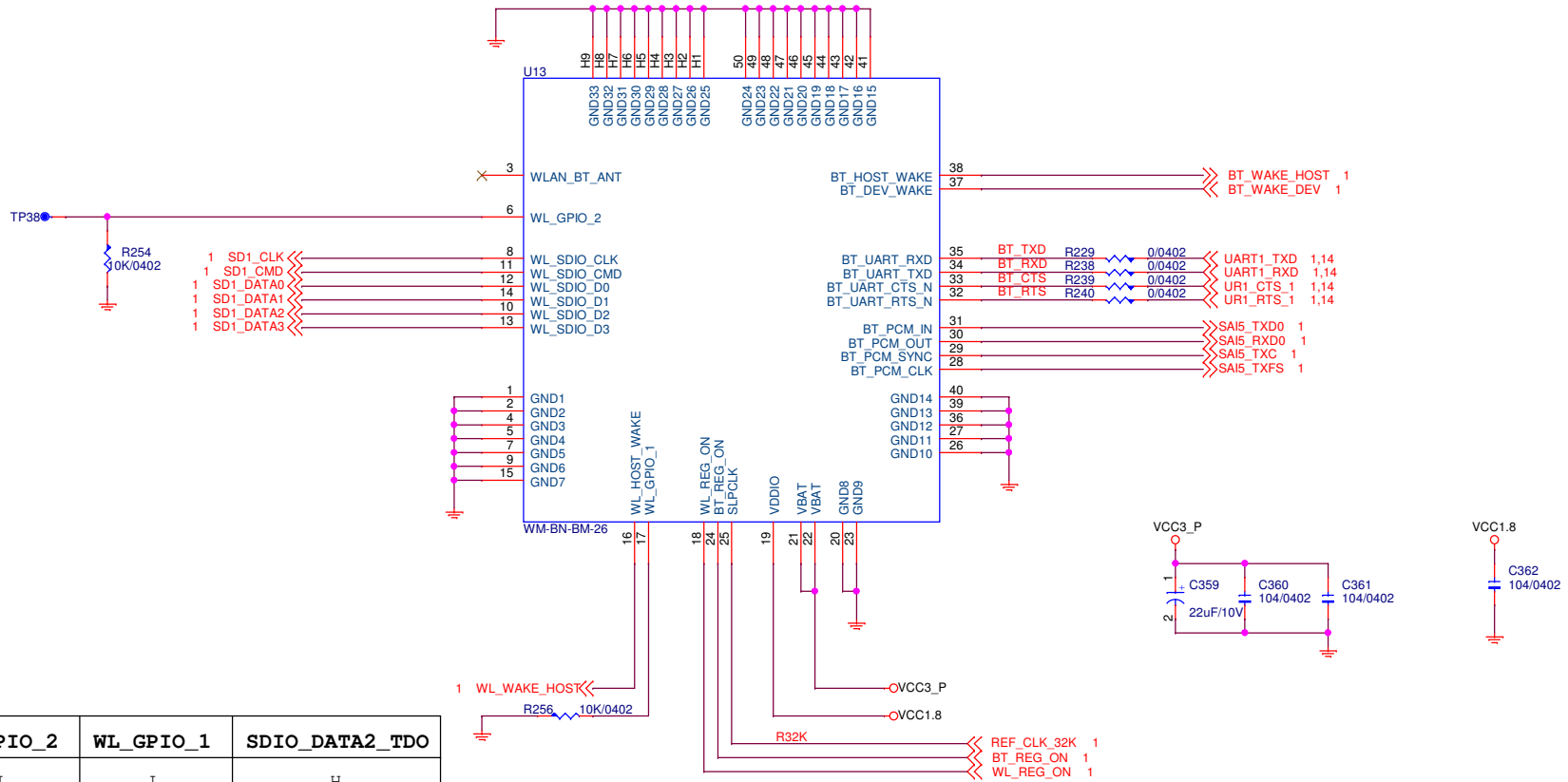


## DMP ELECTRONICS INC.

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COM3&4 RS232/485		
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# 2.4G WIFI Module

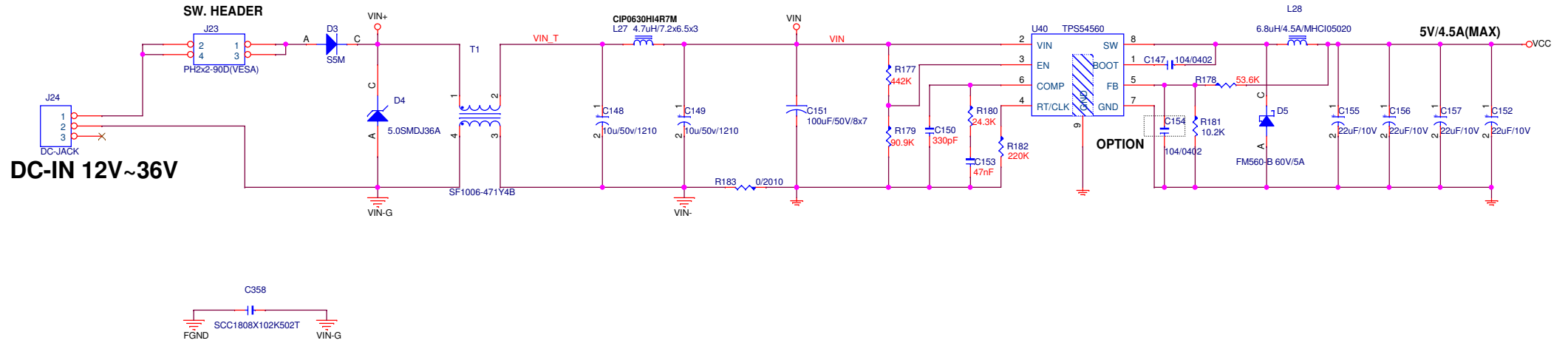


Option	WL_GPIO_2	WL_GPIO_1	SDIO_DATA2_TDO
SDIO	L	L	H
JTAG over SDIO (Default)	L	H	H
JTAG over BT PCM	H	L	H
SWD over GPIO[2:1]	H	H	H
gSPI	X	X	L

**DMP ELECTRONICS INC.**

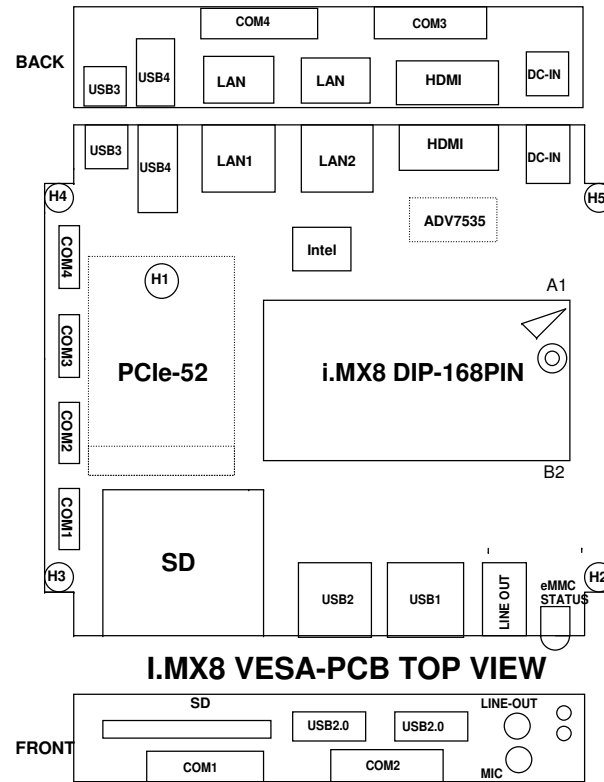
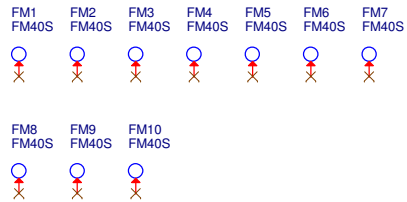
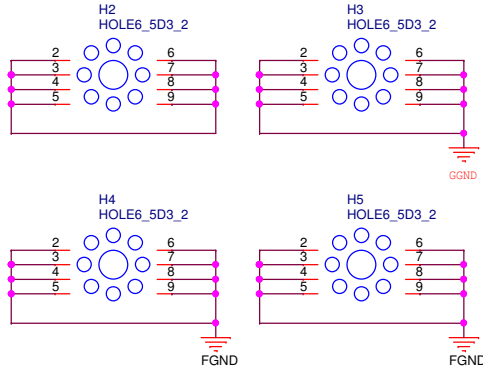
Title		WIFI&Bluetooth
Size	Document Number	Rev
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# DC POWER



<b>DMP ELECTRONICS INC.</b>		
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# Placement



**DMP ELECTRONICS INC.**



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Placement		
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